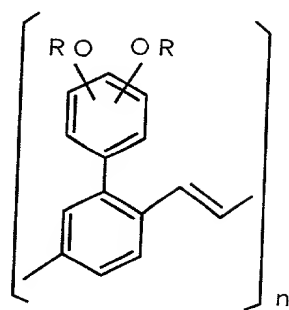
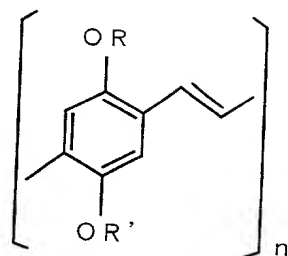


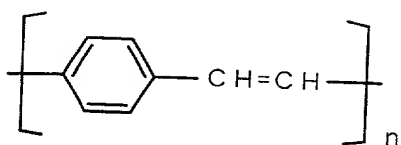
Chem. 1



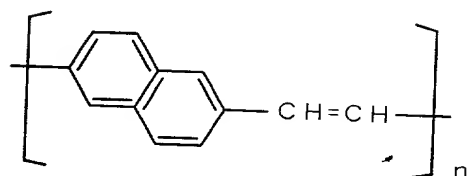
Chem. 2



Chem. 3



Chem. 4



Chem. 5

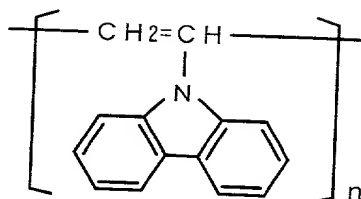


Fig. 1A

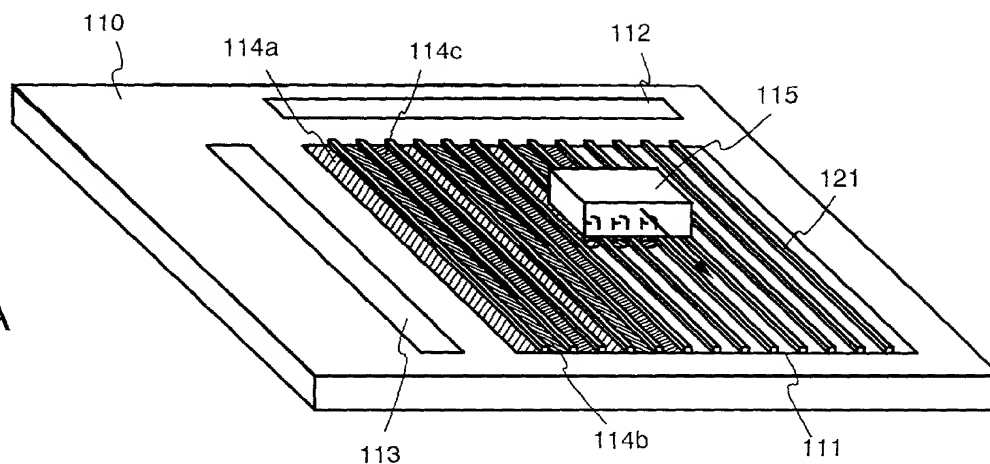


Fig. 1B

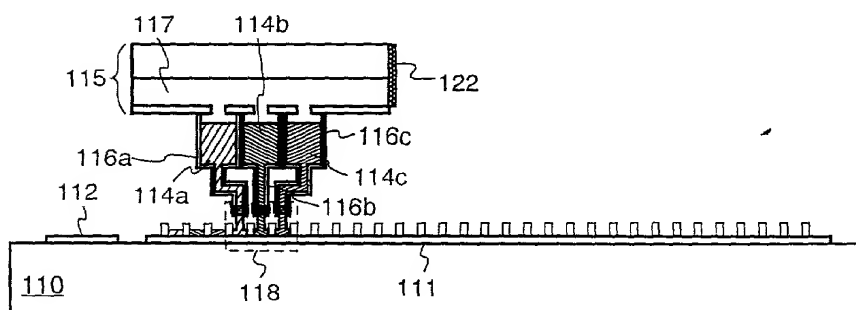
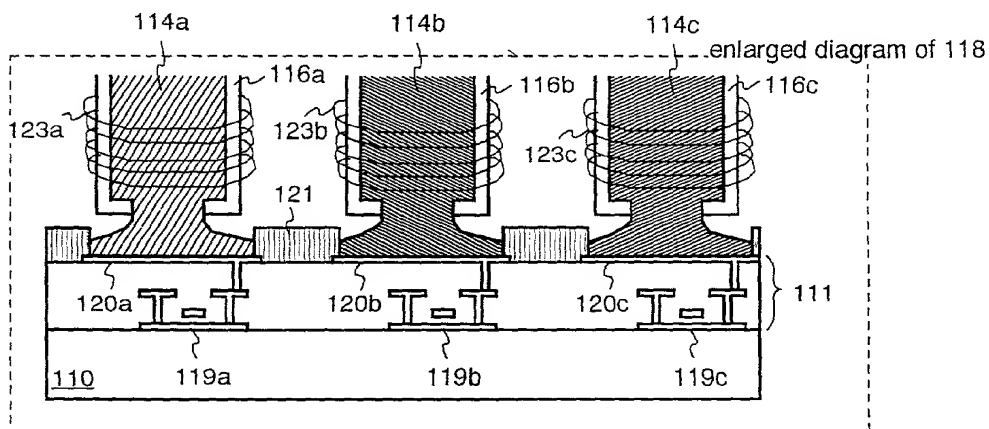


Fig. 1C



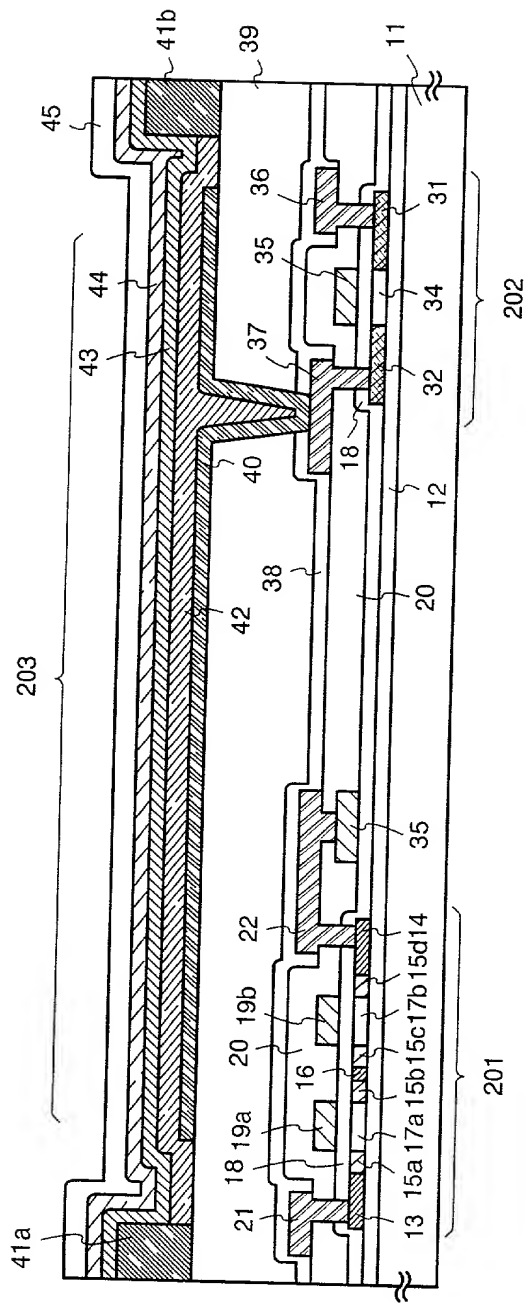


Fig. 2

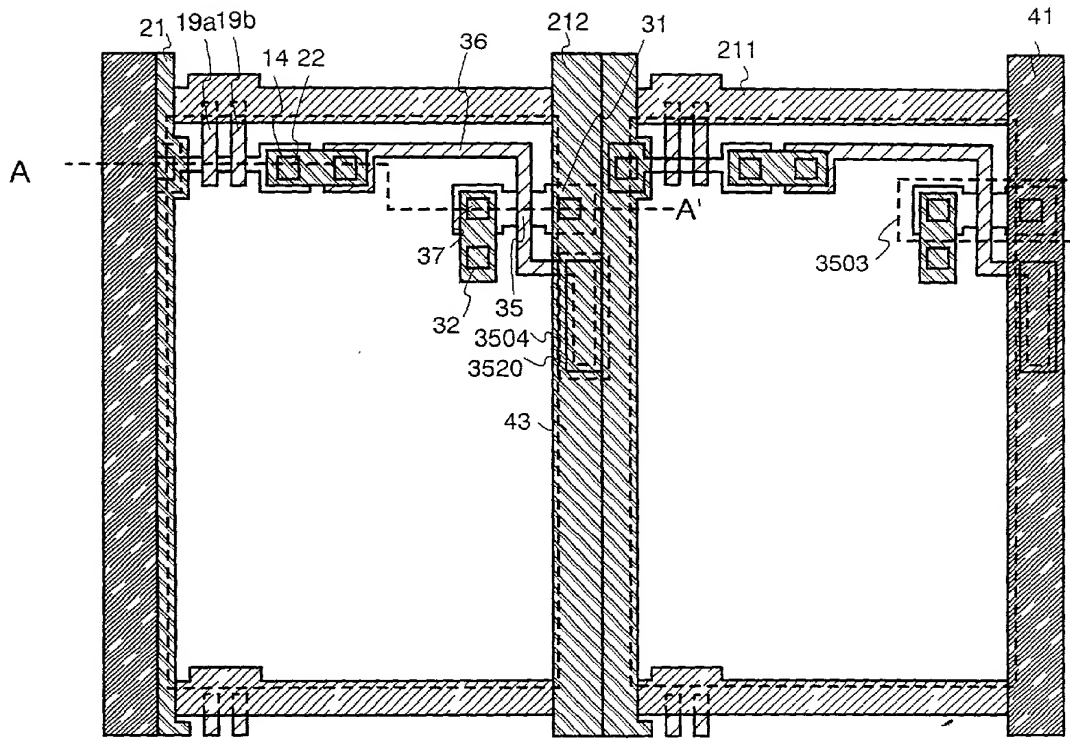


Fig. 3A

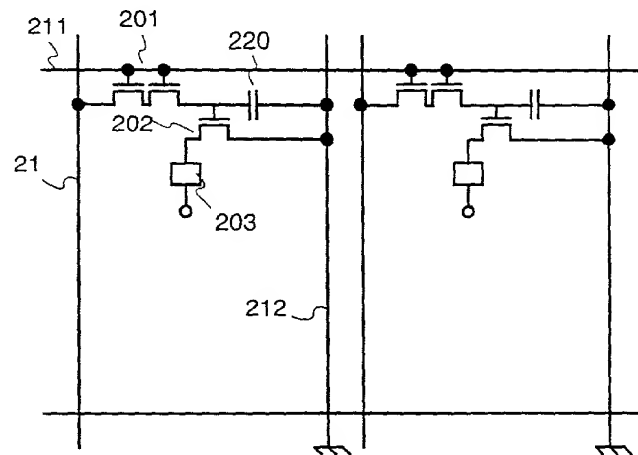
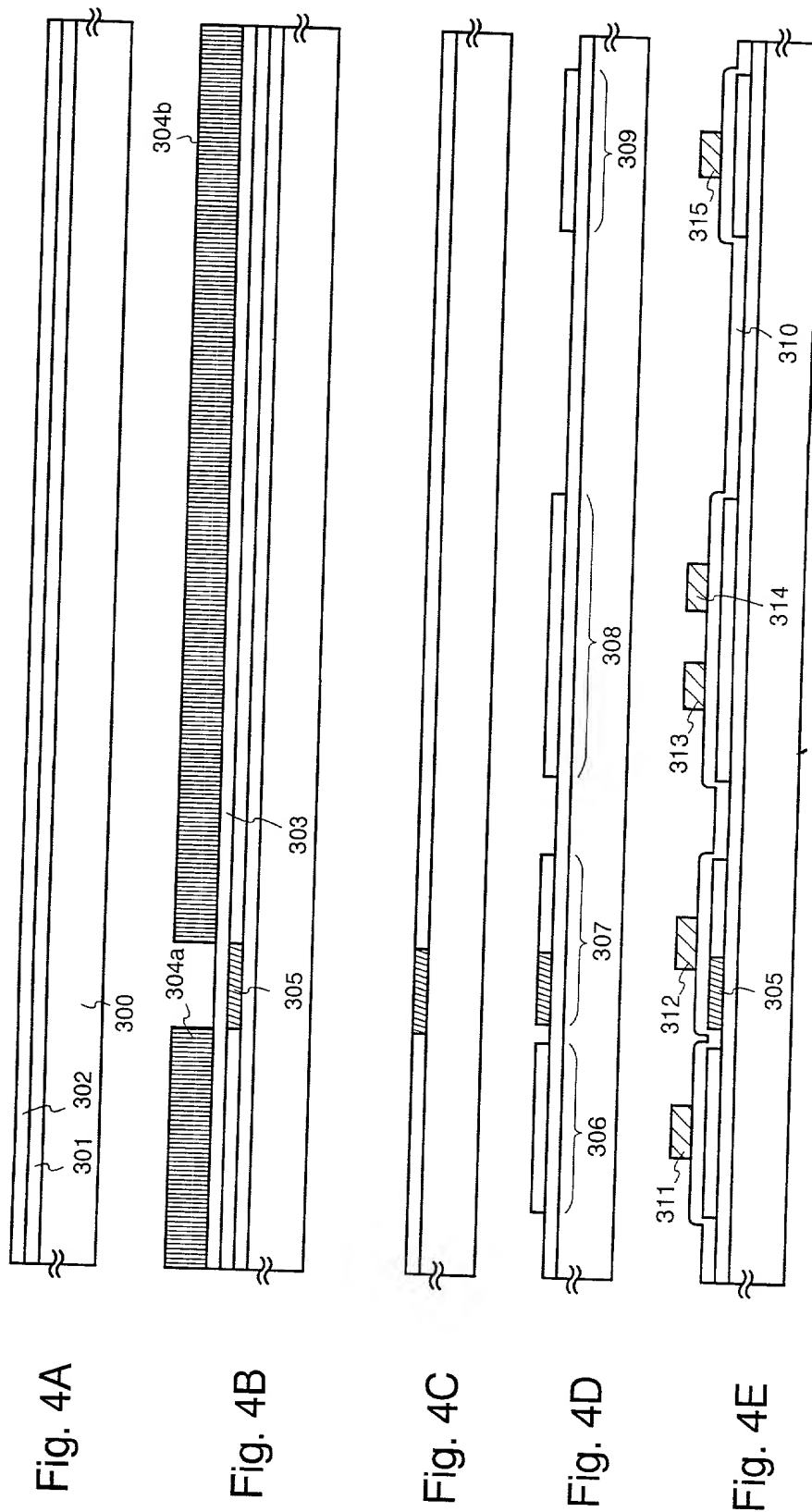


Fig. 3B



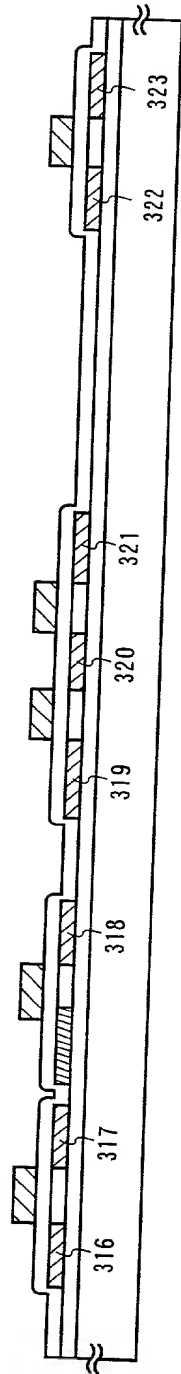


Fig. 5A

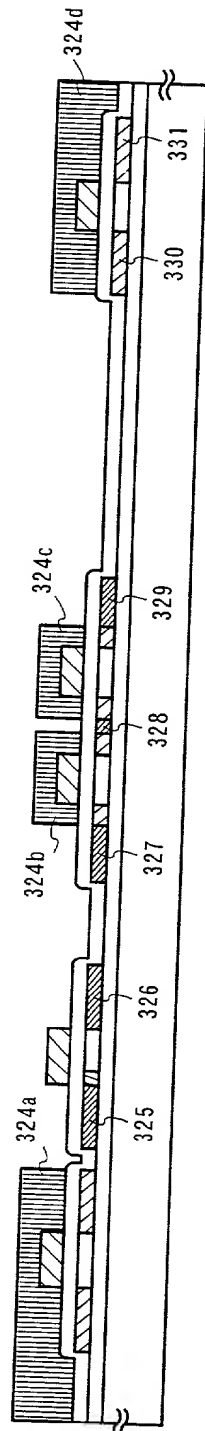


Fig. 5B

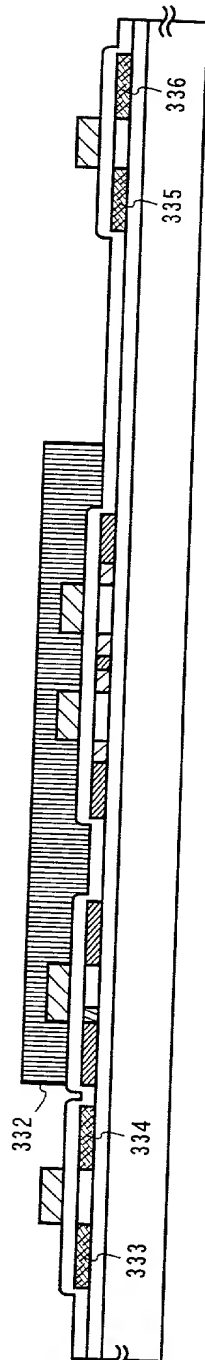


Fig. 5C

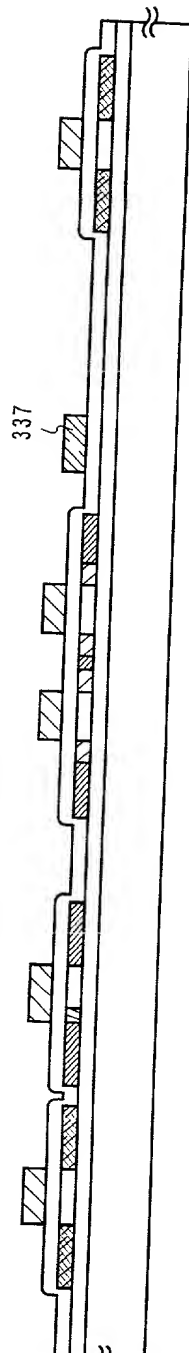


Fig. 5D

316-323: n-type impurity region(c) 324a-324d, 332: Resist Mask 325-329: n-type impurity region(a)
 333-336: p-type impurity region(a) 337: Gate Wiring

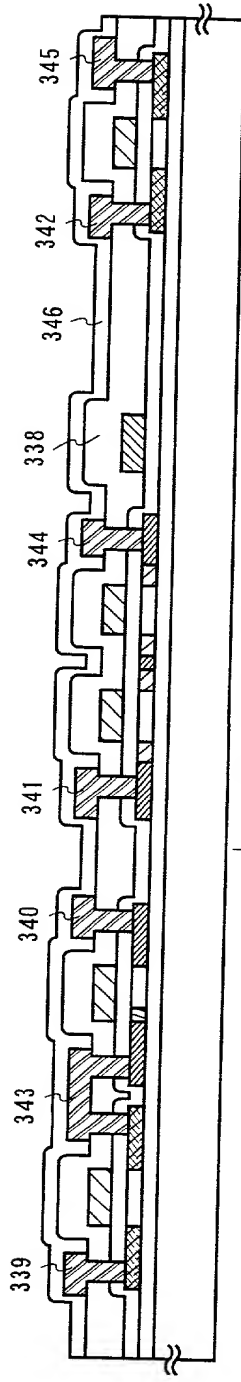


Fig. 6A

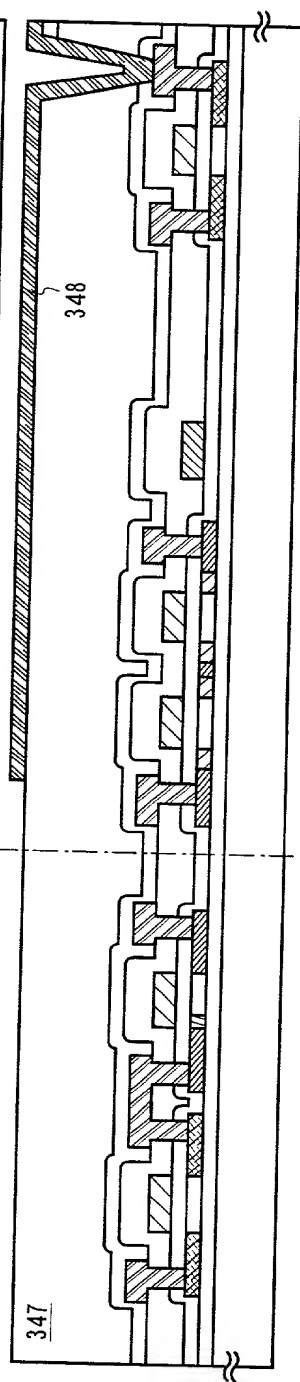


Fig. 6B

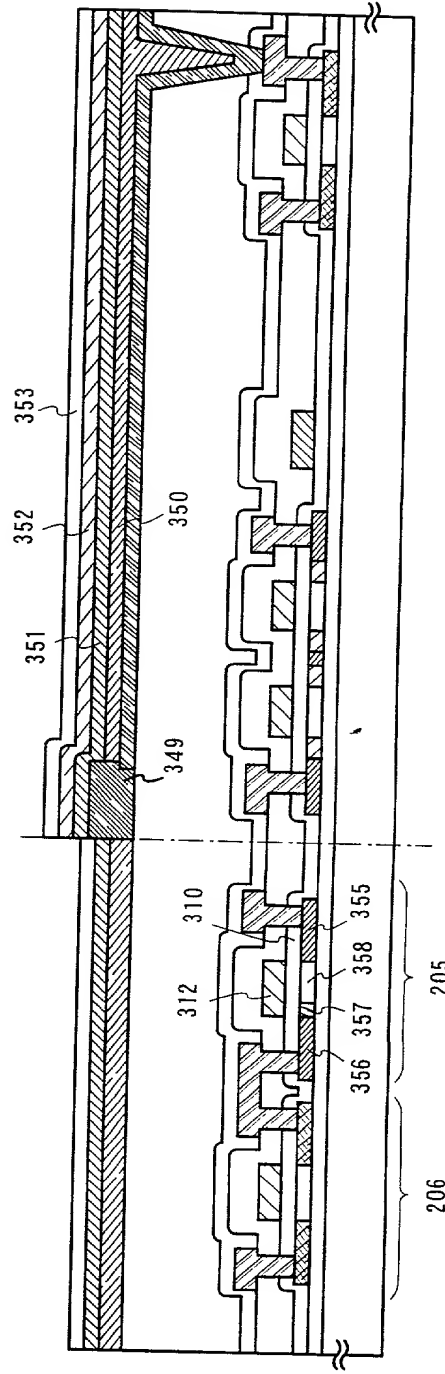
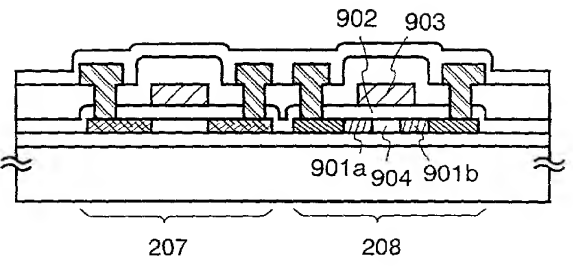
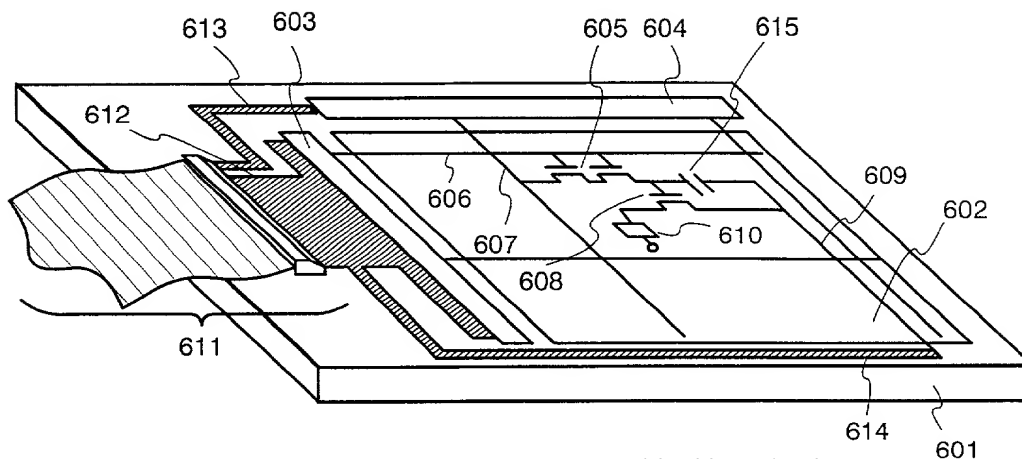


Fig. 6C

338: first interlayer insulating film 339-342: source wiring 343-345: drain wiring
 346: first passivation film 347: second interlayer insulating film 348: pixel electrode
 349: bank 350: EL layer 351: cathode 352: protecting electrode 353: second passivation film

Fig. 7





601:substrate 602:pixel portion 603:gate side driver circuit
 604:source side driver circuit 605:switching TFT 606:gate wiring
 607:source wiring 608:current control TFT 609:electric power supply
 610:EL element 611:FPC 612-614:input/output terminal 615:capacitor

Fig. 8

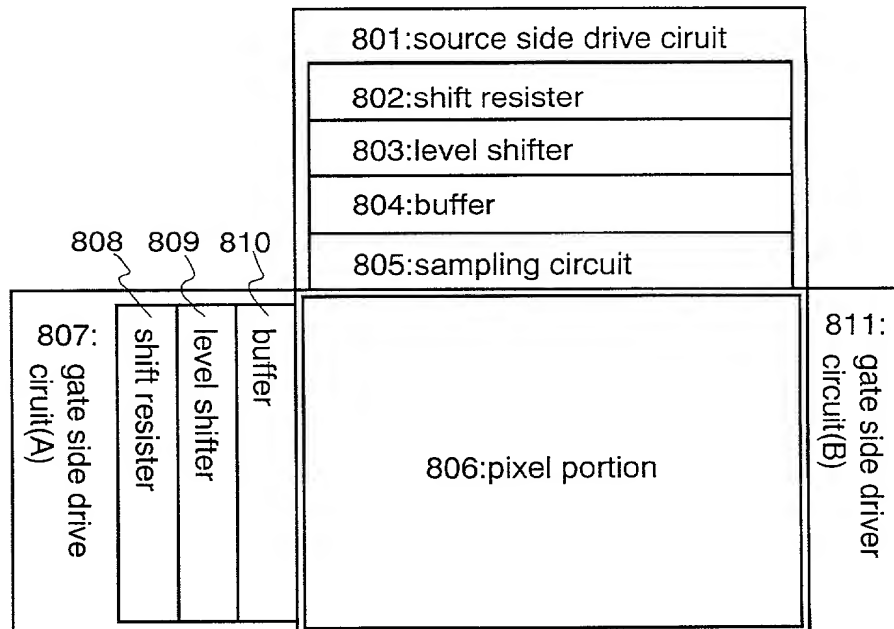


Fig. 9

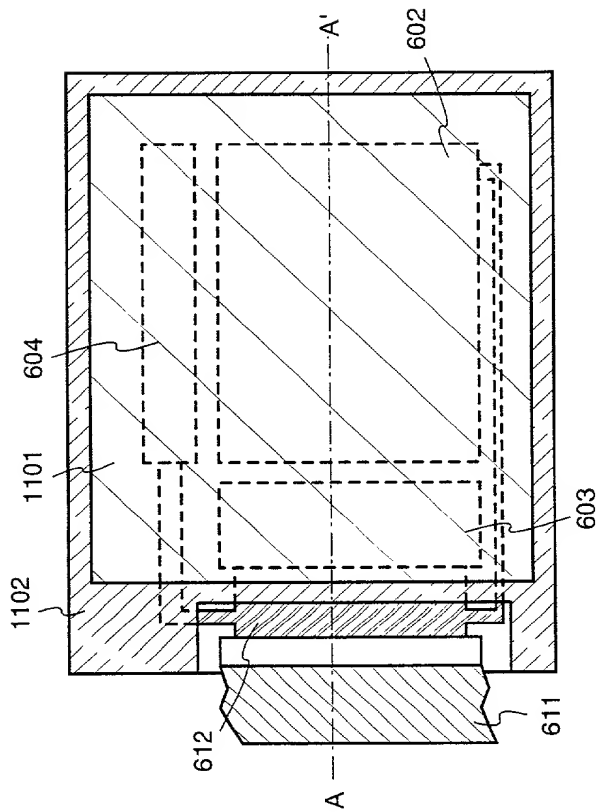


Fig. 10A

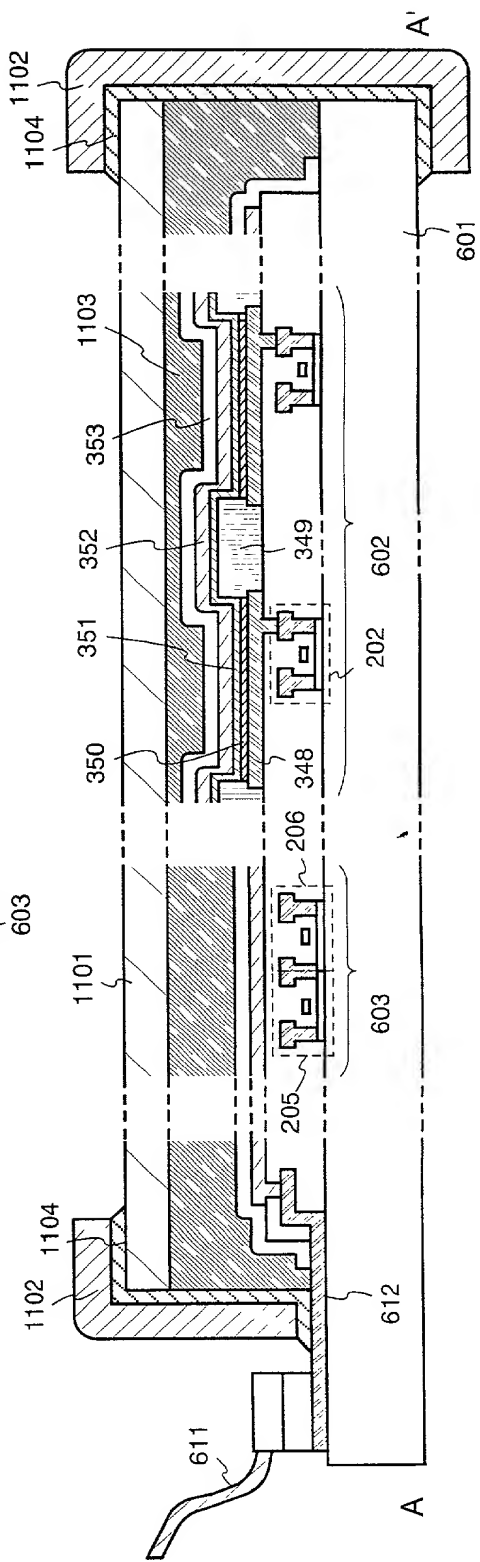


Fig. 10B

Fig. 11A

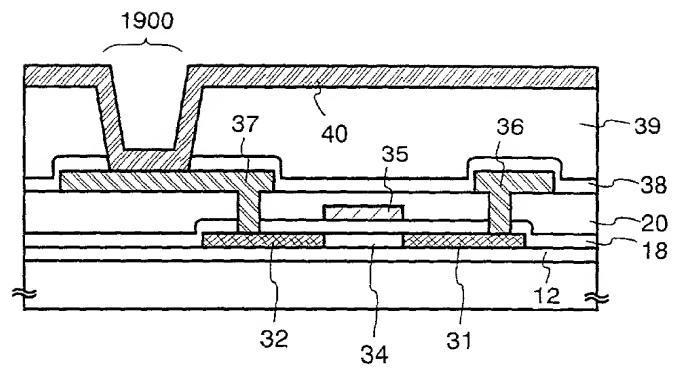


Fig. 11B

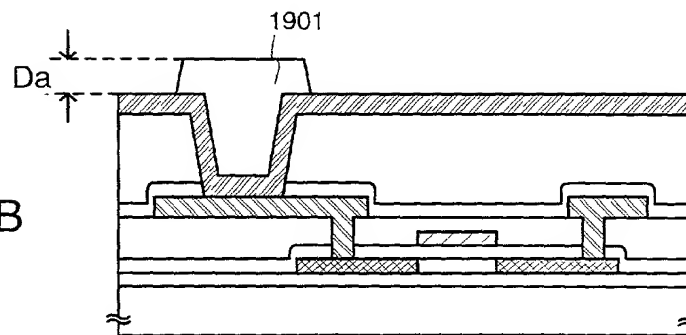
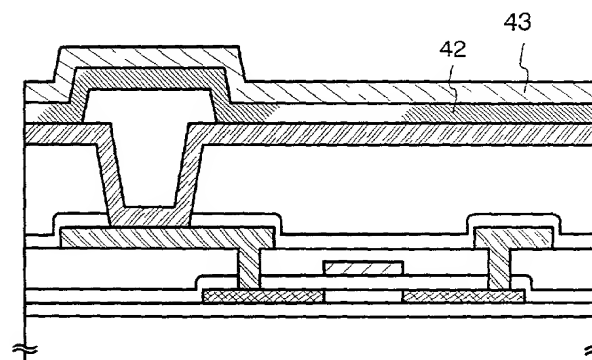


Fig. 11C



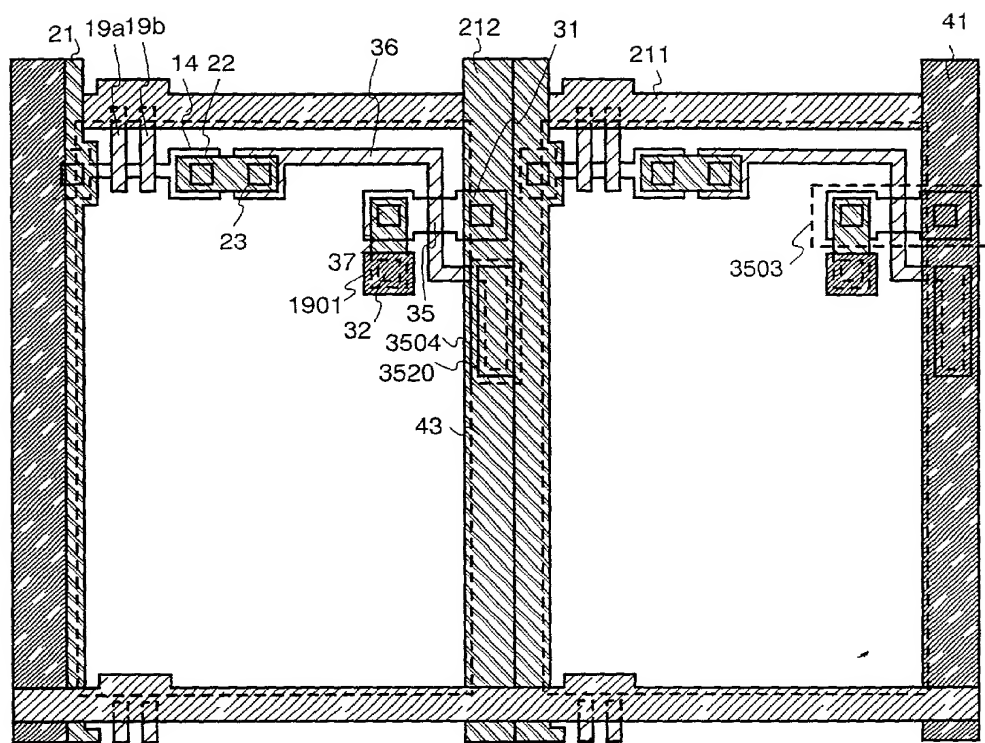
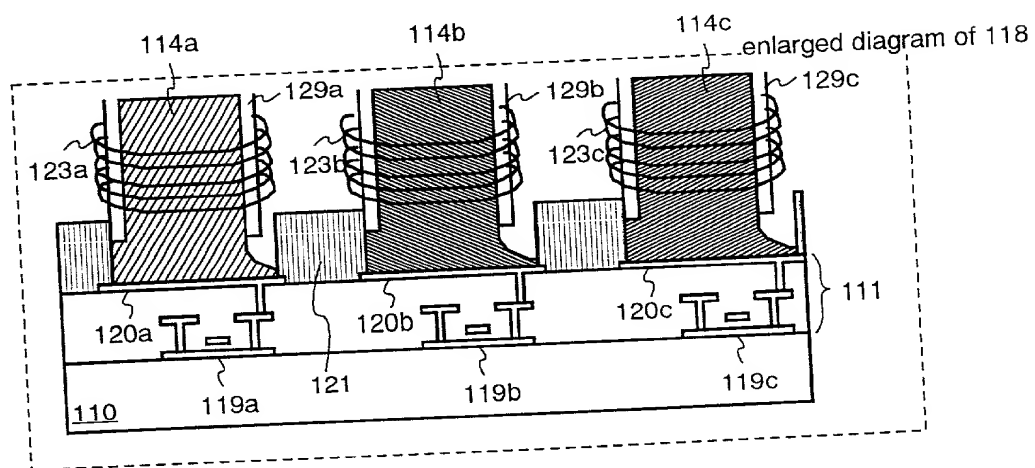
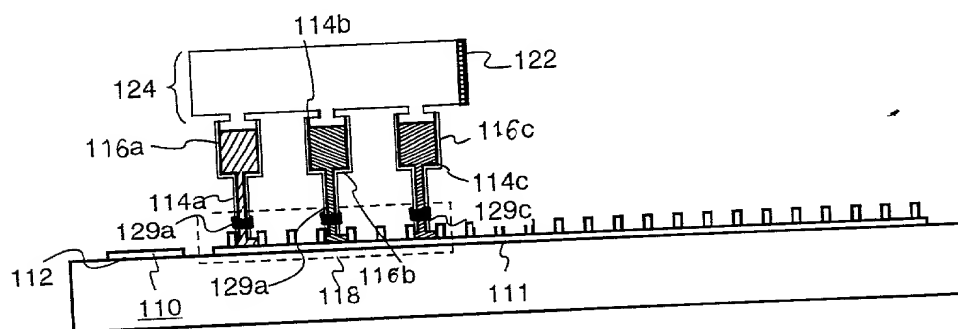
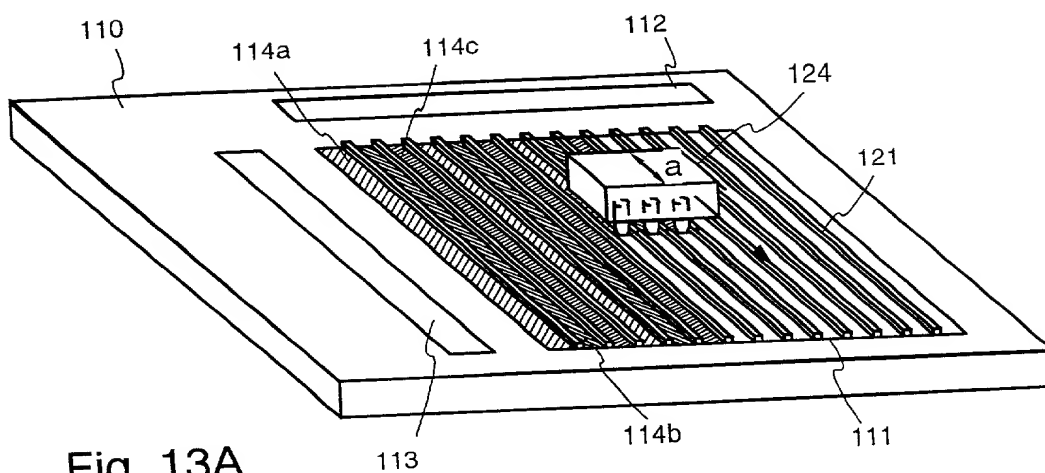


Fig. 12



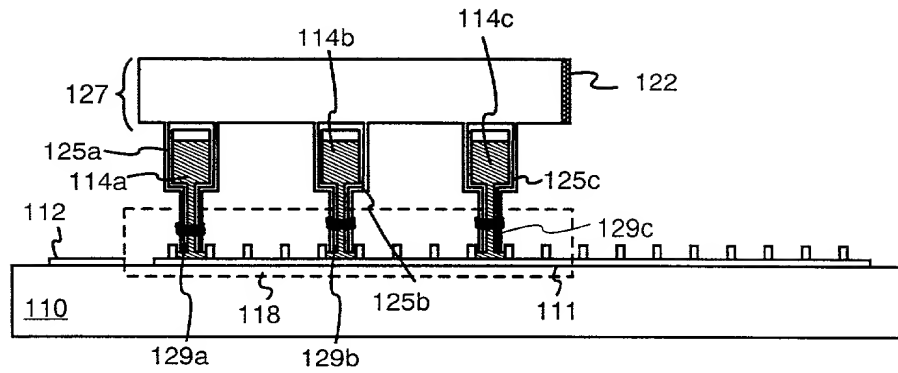


Fig. 14A

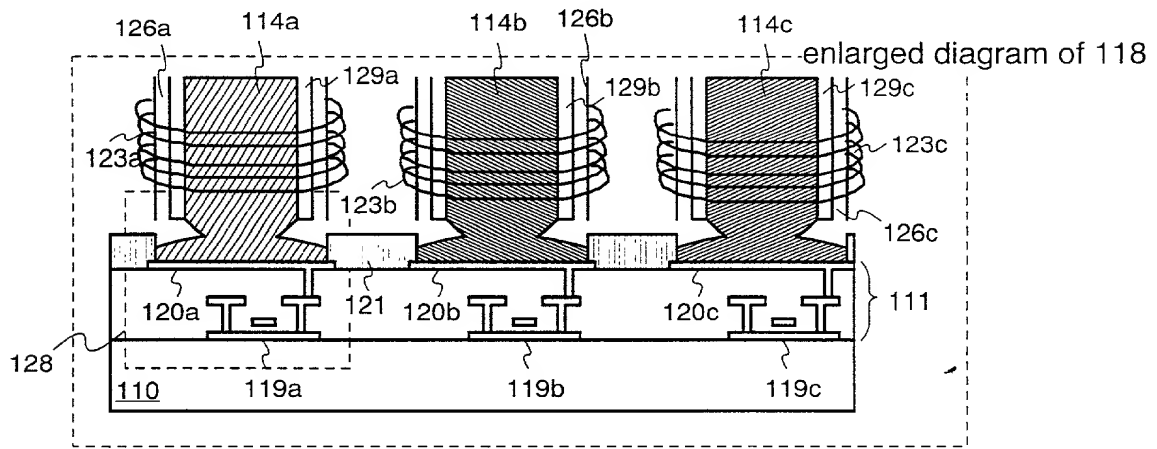


Fig. 14B

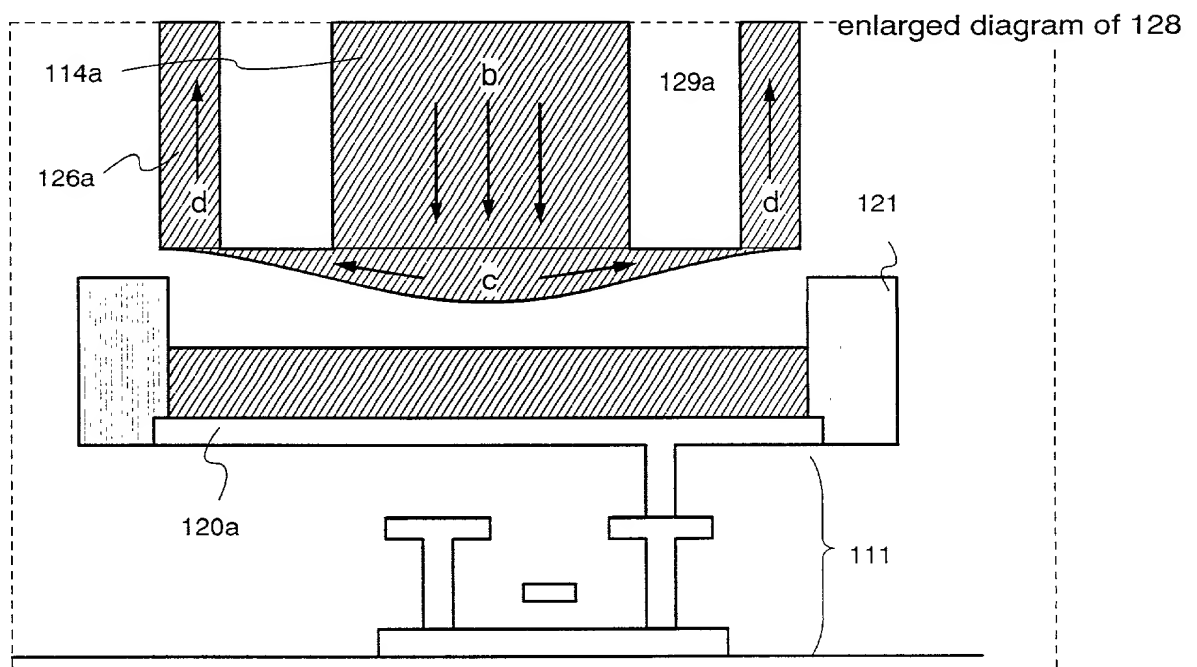


Fig. 14C

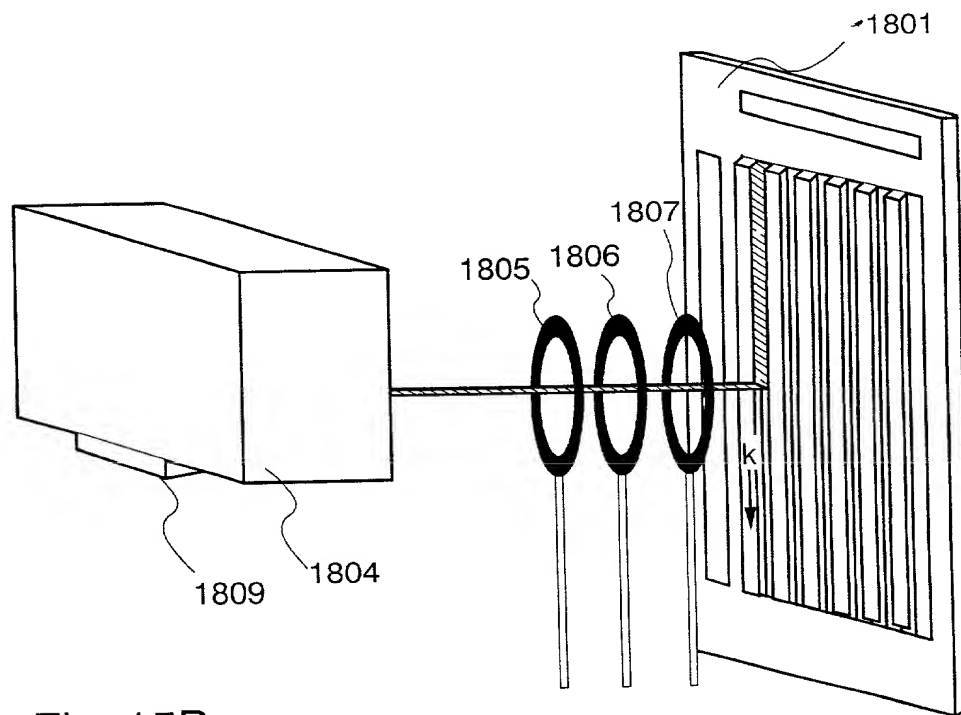
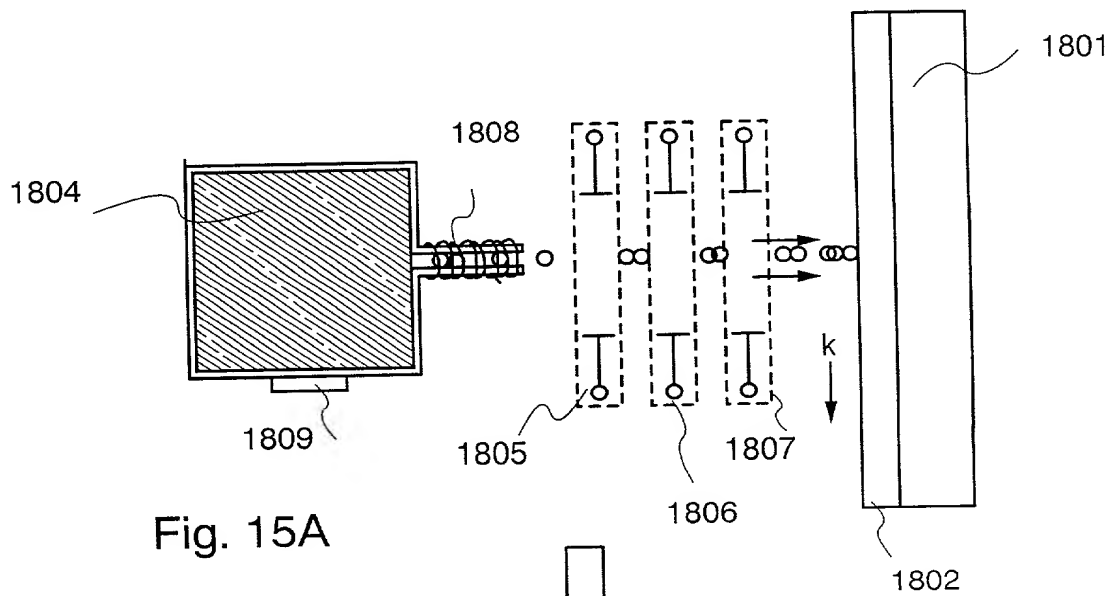


Fig. 16A

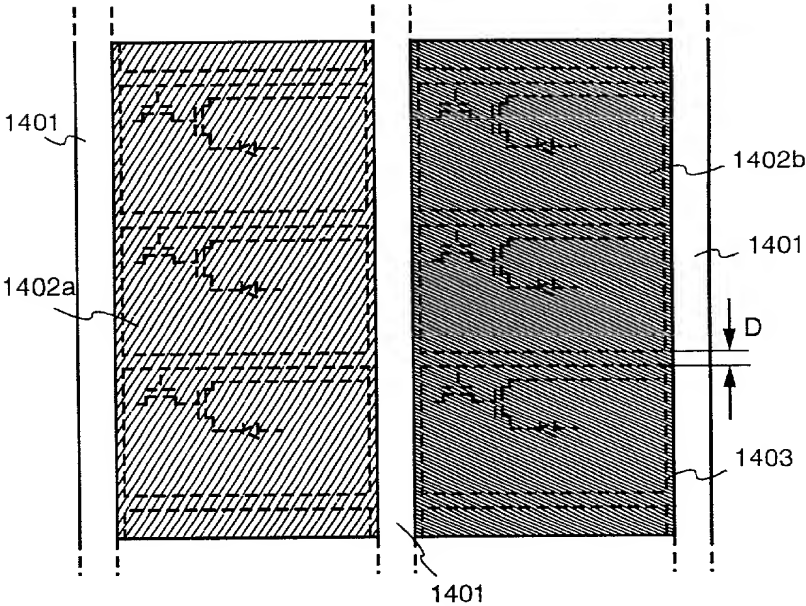
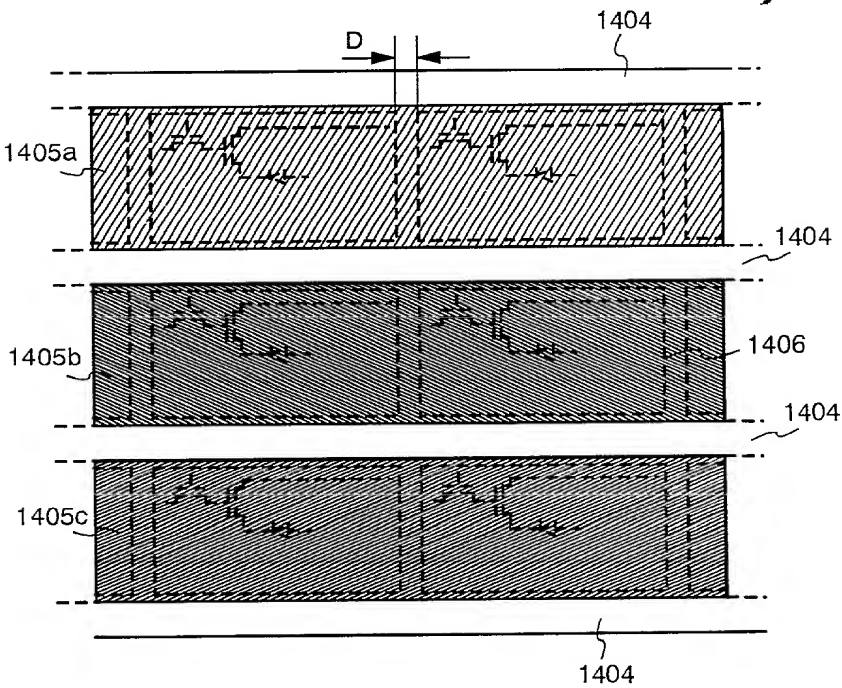


Fig. 16B



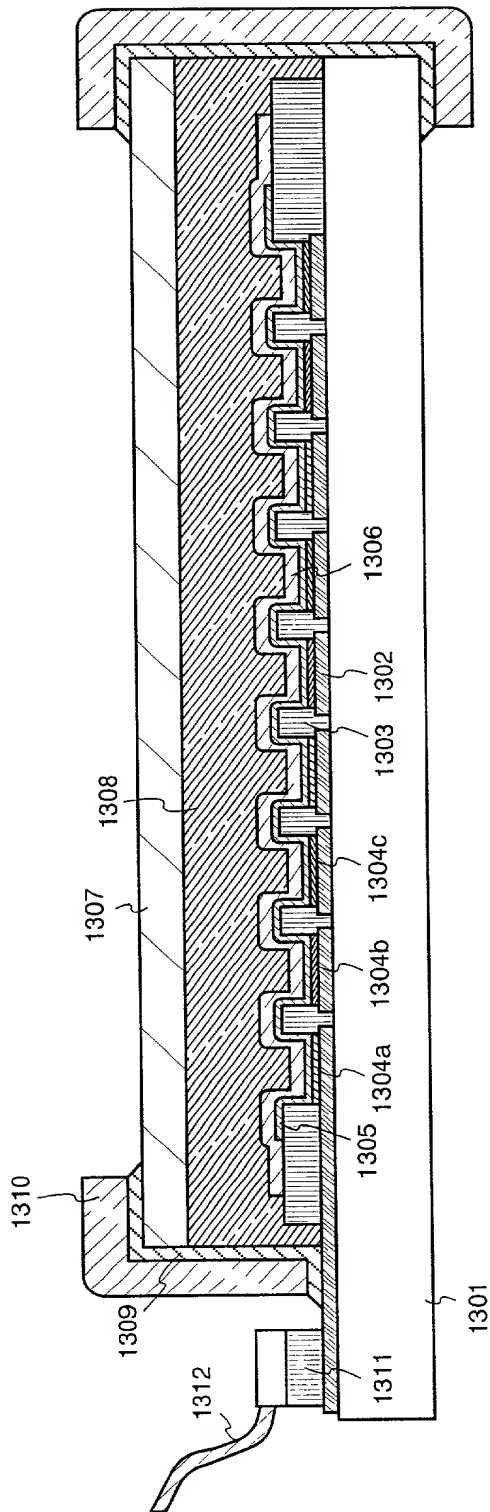
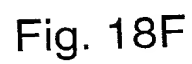
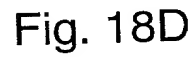
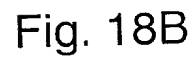


Fig. 17



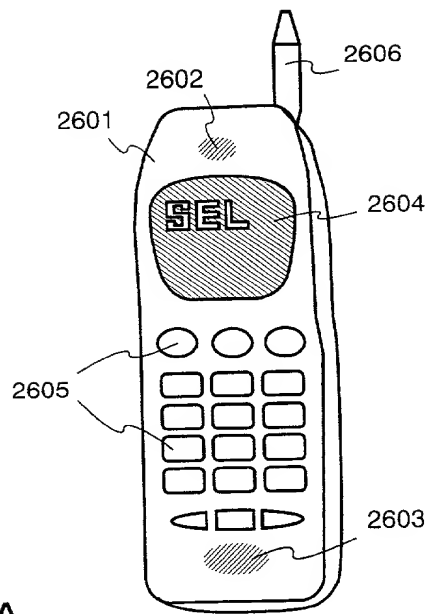


Fig. 19A

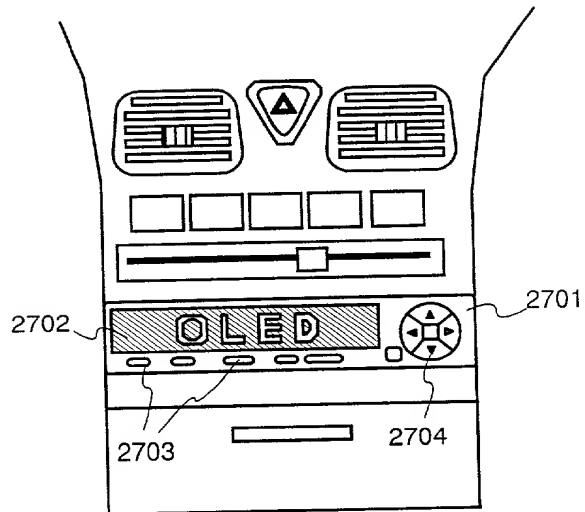


Fig. 19B

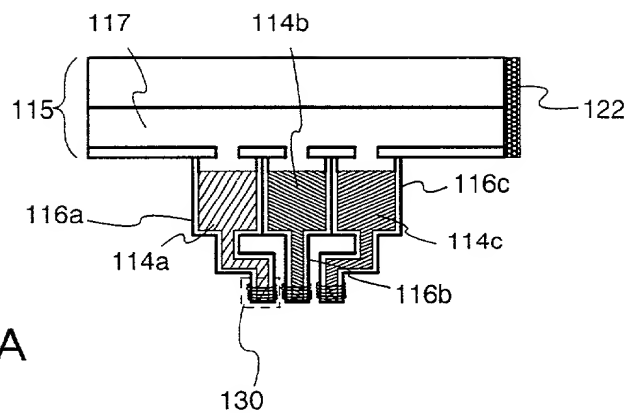


Fig. 20A

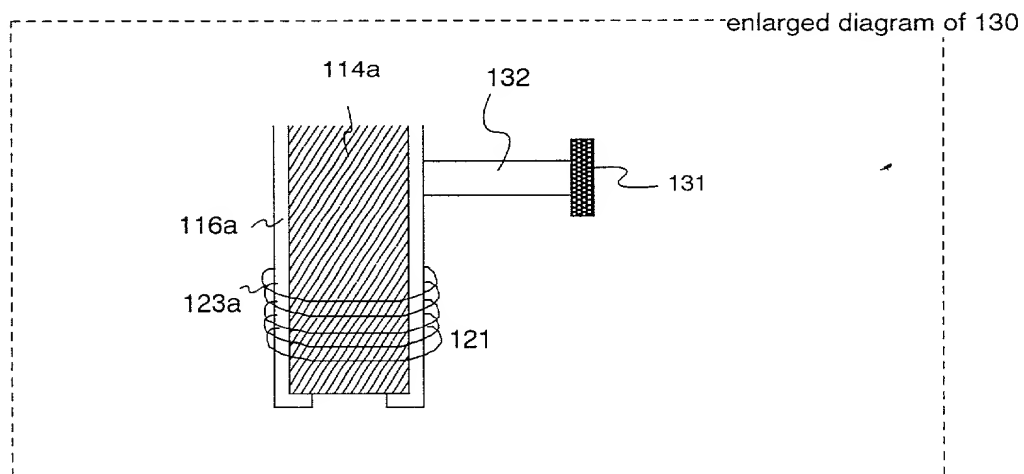


Fig. 20B